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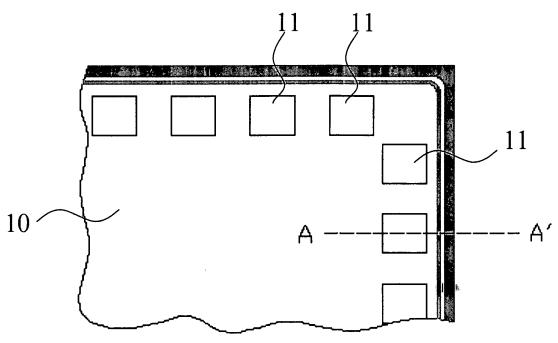
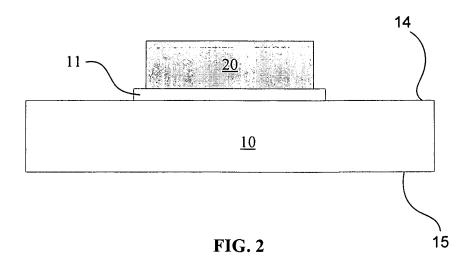


FIG. 1



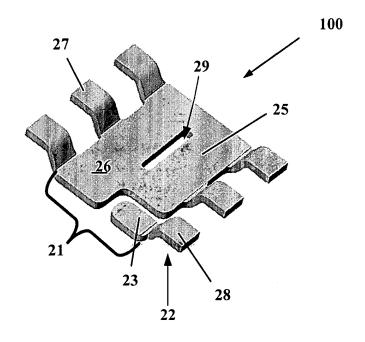


FIG. 3

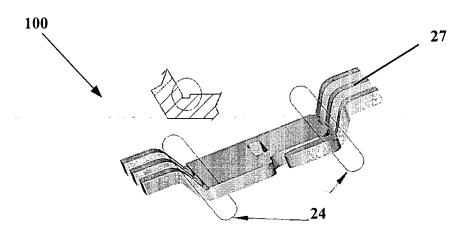


FIG. 4

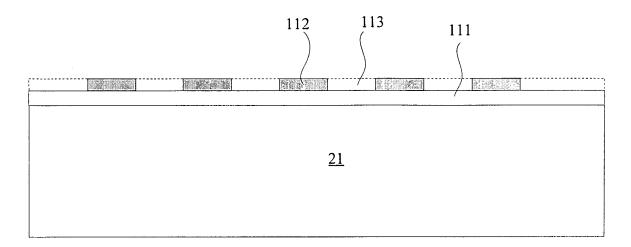


FIG. 5

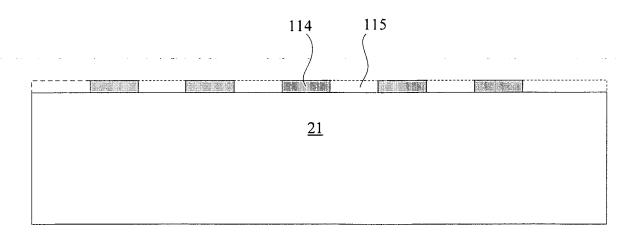
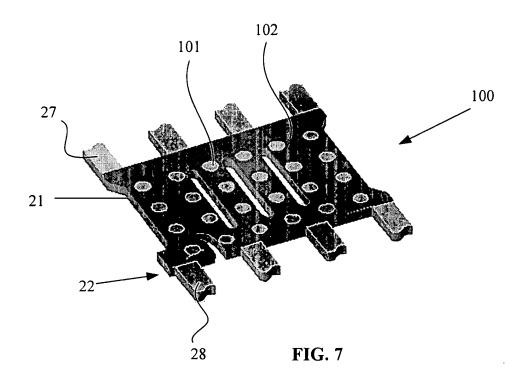
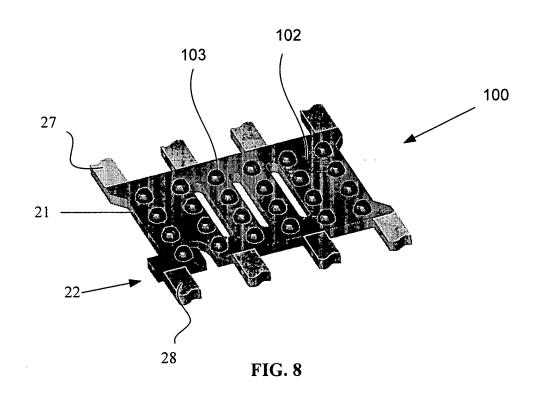
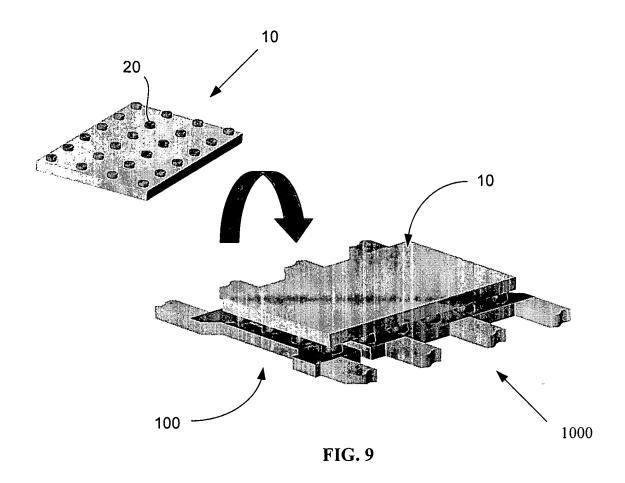
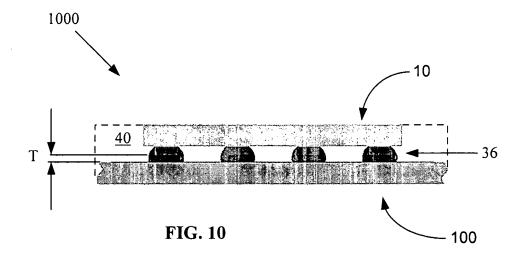


FIG. 6









SELECTIVE PLATING LEADFRAME

FULLY SOLDERABLE LEADFRAME PAD

SELECTIVE SOLDERABLE LEADFRAME PAD

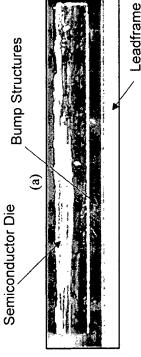


Fig. (a) is a photo of attached die with metal stud in a fully solderable leadframe pad.

Metal Stud

Semiconductor Die

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Semiconductor Die Bump Structures (b)

Fig. (b) is a photo of attached die with metal stud in a selective solderable area of leadframe pad.

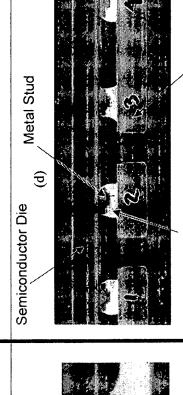


Fig. (d) is a SEM photo of cross-sectioned die with metal stud attached in a selective solderable leadframe pad. The solder is limited to a certain area of the leadframe and the solder thickness between metal stud and leadframe is thick.

spreads widely in the pad and the solder thickness between

metal stud and leadframe is thin.

Fig. (c) is a SEM photo of cross-sectioned die with metal stud attached in a fully solderable leadframe pad. The solder

Solder

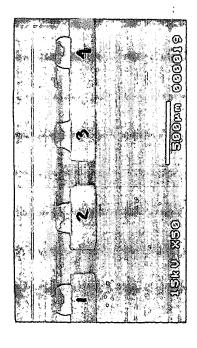
Leadframe

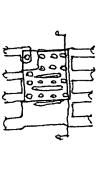
Solder

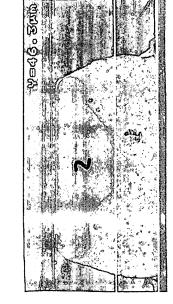
Leadframe

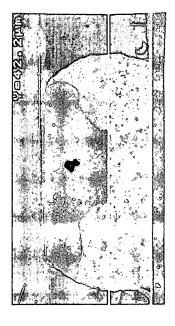
FIG. 11

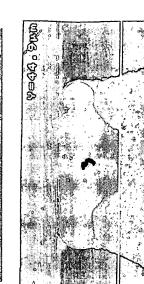
SELECTIVE PLATING LEADFRAME CROSS-SECTION (SELECTIVE PLATING FRAME)

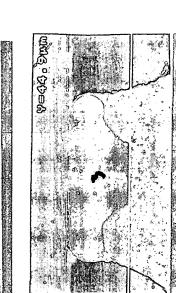






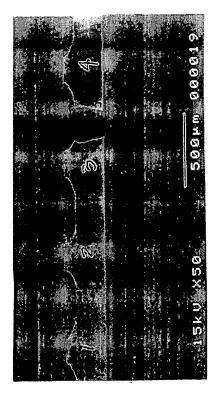


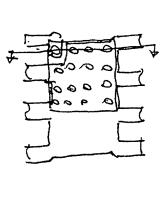




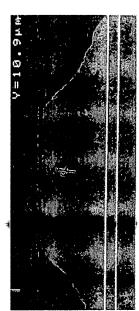
SELECTIVE PLATING LEADFRAME

CROSS-SECTION (NON - SELECTIVE PLATING FRAME)









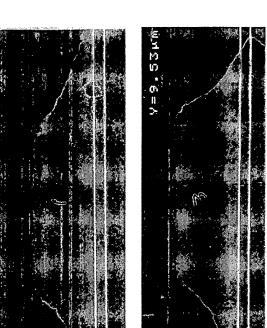


FIG. 13

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